L Number	Hits	Search Text	DB	Time stamp
6	2879	(ic or (integrated adj circuit)) same memory same counter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 08:17
8	3	(ic or (integrated adj circuit)) same memory same counter same indicator same threshold	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 09:10
9	5	(ic or (integrated adj circuit)) same memory same counter same (disabl$4 stop$4) same threshold	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 08:56
10	2	(ic or (integrated adj circuit)) same (disabl$4 stop$4) same counter same indicator same threshold	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 09:06
11	322	memory same counter same (disabl$4 stop$4) same threshold	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 09:26
7	77	(ic or (integrated adj circuit)) same memory same counter same indicator	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 09:19
12	8	(ic or (integrated adj circuit)) same memory same counter same indicator same disabl$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 09:11
17	1	(ic or (integrated adj circuit)) same memory same counter same indicator same maximum same minimum	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 09:21
18	10	(ic or (integrated adj circuit)) same memory same counter same indicator same state	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 09:21
19	10	(ic or (integrated adj circuit)) same memory same counter same indicator same value	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 09:24
26	3	(memory same counter same (disabl$4 stop$4) same threshold) and 235/$7.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 09:29